



Job Description

Failure Analysis Engineer

Scope

To be part of an energetic team ensuring first-rate quality to Fresco's customers by performing failure analysis on mixed-signal integrated circuit products.

Key Accountability Areas

1. Prompt, efficient, and accurate analysis of suspected defects in Fresco's products.
2. Facilitate Failure Analysis (FA) activities to identify root cause of verified defects.
3. Facilitate FA activities to implement corrective action to achieve Fresco's DPPM target.

Typical Activities

- Drive FA activities for customer RMAs, internal quality, yield improvement, or reliability qualification failures.
- Perform analysis activities using Fresco equipment and manage external vendors during analyses.
- Interpret and validate external analysis vendor reports to ensure accuracy and completeness.
- Establish new FA techniques to support new products as introduced.
- Create root cause FA reports for both internal and external customers.
- Review failure history, identify trends, provide a trend and data analysis, suggest debug procedures, initiate vendor FA and trigger corrective actions for failures.

Requirements

- Bachelor of Science in Electrical Engineering, Physics, or equivalent.
- 3-5 years of comparable experience in semiconductor failure analysis, reliability, yield enhancement, or product engineering.
- Knowledgeable of both bipolar and CMOS processes and circuit analysis and familiar with operation and application of analytical tools used in failure analysis.
- Excellent written and verbal communication skills.
- Ability to liaise with customers on solving quality issues with a sense of urgency.