



Job Profile

Staff Engineer, DSP

Scope

You will innovate competitive architectures and algorithms to advance the state of the art embedded within our TV tuner/demodulation semiconductor products.

Key Accountability Areas

1. Transform conceptual problems into working solutions.
2. Collaborate with the DSP design team and other departments to ensure that proposed solutions satisfy customer requirements.
3. Establish best practices to enable rapid development of algorithms destined for IC implementation.

Typical Activities

- Define technical questions and specifications.
- Prepare technical architectures and analysis to support design choices.
- Review and evaluate designs and proposals of engineers.
- Develop and maintain a simulation environment.
- Design and debug behavioral models for simulations.
- Document test plans and create test vectors to ensure adequate performance.
- Ensure that ASIC implementation and behavioural simulation models are equivalent.
- Meet with project leads/managers to discuss technical matter, and to offer estimates on effort required for projects.

Requirements

- Strong technical background in TV system architecture, communications and filter design.
- Proven track record for developing algorithms for semiconductors.
- Proficient at programming C/C++ and Matlab.
- Innovative and clear thinker.
- Outstanding communication skills.
- Effective team collaborator.
- Self-motivated.
- Leadership qualities.
- 12+ years of R&D experience in a related industry.
- Bachelor degree in Electrical Engineering (M.Sc / Ph.D degree an asset).