



Job Profile

Staff Analog/RF IC Design Engineer

Scope

Fresco Microchip requires a Staff Analog/RF IC Design Engineer to participate in the design and technical leadership of advanced tuner and demodulator products.

Key Accountability Areas

1. Deliver analog circuits that meet product specifications, draw low power, are area efficient and on schedule.
2. Provide leadership to the analog design team to ensure technical success, timely completion and efficient effort.
3. Work with leaders from other groups to ensure product success.

Typical Activities

- Define analog/RF IC design specifications and/or deliver to specifications and to schedule.
- Establish architectures for systems and high-performance circuit blocks.
- Conduct and participate in detailed design reviews.
- Lab evaluation of analog and RF circuits.
- Provide technical leadership within a team environment, sharing common goals, strategies and information.
- Communication of progress with appropriate frequency and detail to superiors, peers and subordinates.

Requirements

- Seven years' experience in the field and preferably a Master's or Ph.D. degree.
- Proven experience of project and team leadership. Ability to plan projects and lead teams to deliver on schedule.
- A proven track record in the field of analog/RF IC design, with expertise in such blocks as low-noise amplifiers, mixers, PLLs, ADCs, DACs, amplifiers and potentially other high-speed blocks.
- Expertise in analog design techniques, circuit fundamentals, biasing, matching and reliability issues.
- Expertise in analog signal processing concepts, such as filtering, feedback and FFT analysis.
- Understand, identify, and quantify non-idealities in circuit and physical design.
- Understanding of advanced CMOS processes and modelling issues. Familiarity with BiCMOS would be advantageous.

Success Indicators

- Reduced cost and lowered power of projects which are delivered in less time than existing designs.
- First time functionality of complex ICs, architectures and circuit blocks which meet specifications with reduced area, power & cost
- Build the overall technical development of the design team and continuously improve processes to ensure circuit quality and design efficiency.